Fig.1

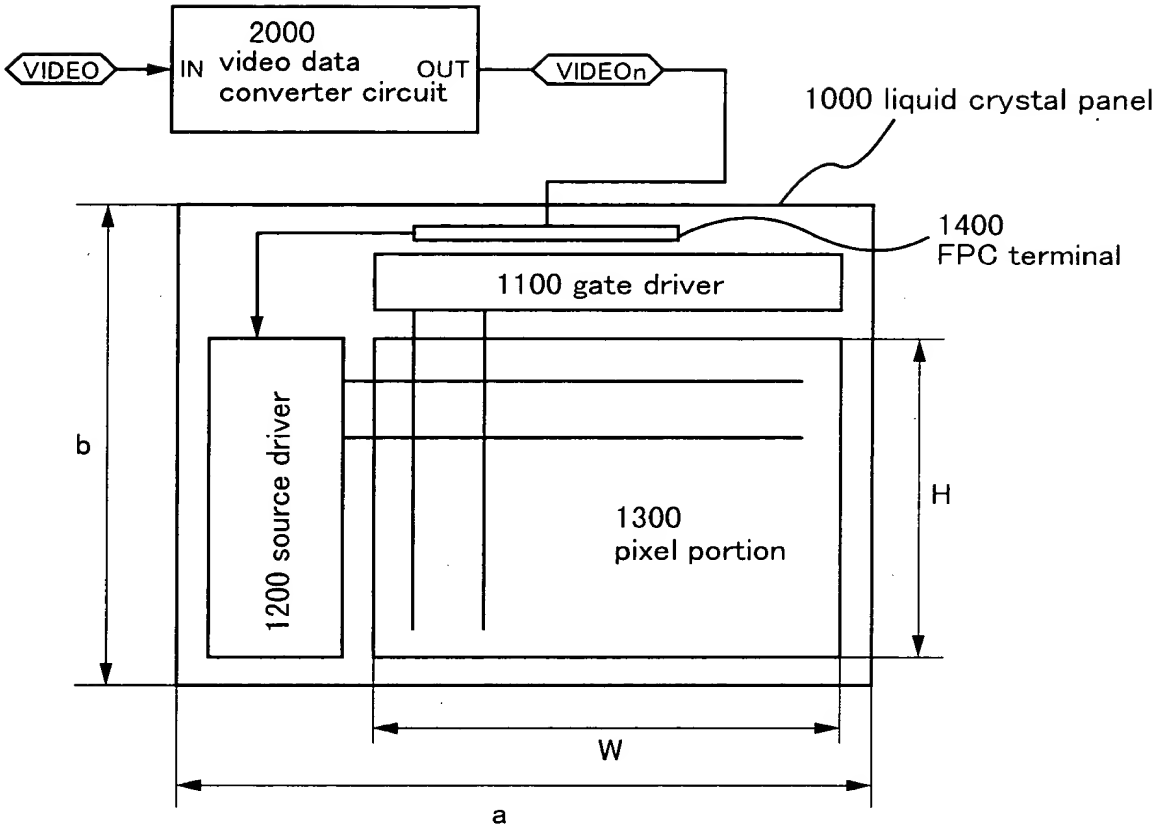

2000
VIDEO
IN
video data converter circuit
OUT
VIDEOn
1000 liquid crystal panel
1400
FPC terminal
1100 gate driver
1200 source driver
1300
pixel portion
b
H
W
a

Fig.2

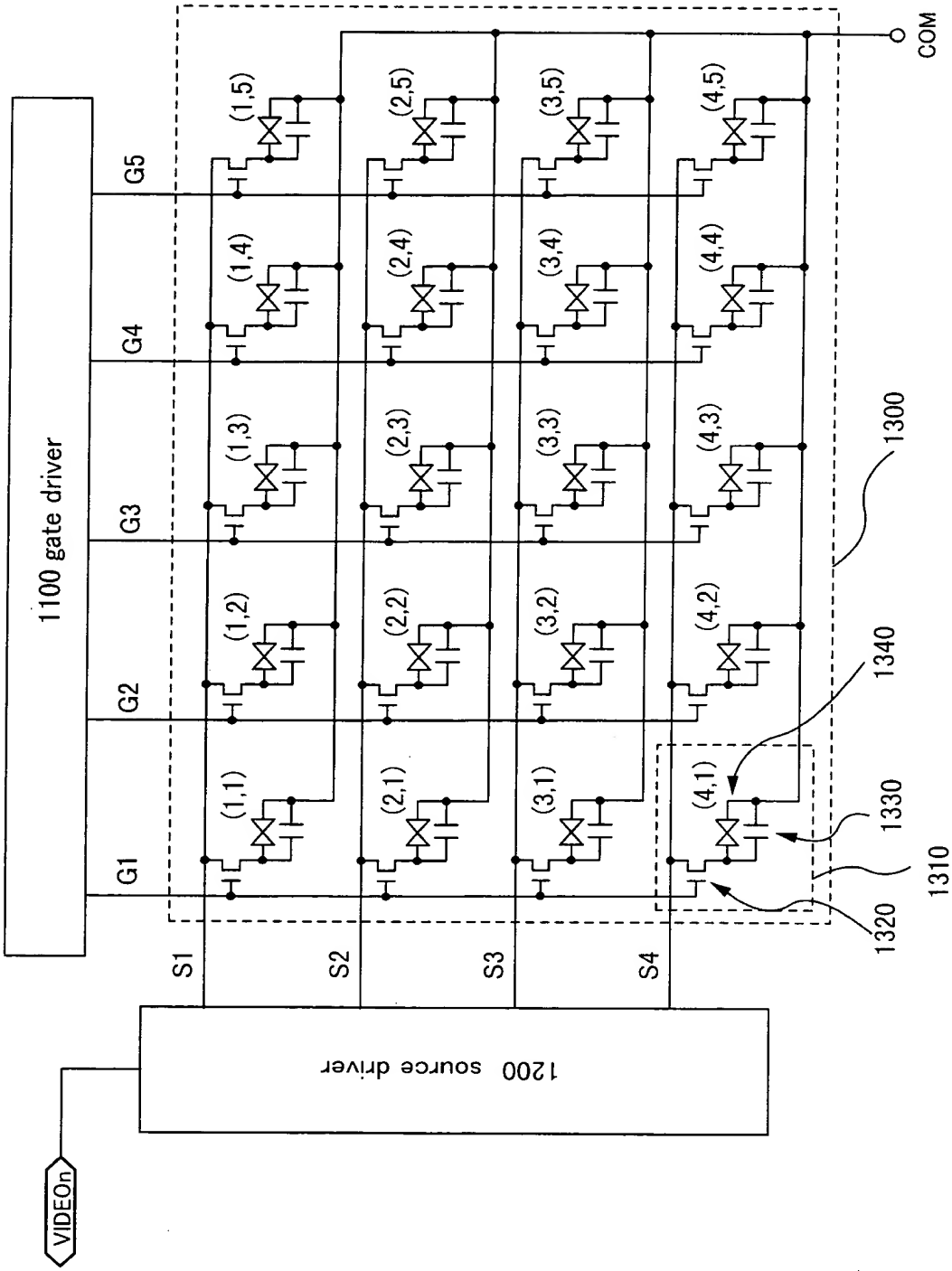

1100 gate driver
1200 source driver
VIDEOn
COM
G1
G2
G3
G4
G5
S1
S2
S3
S4
(1,1)
(1,2)
(1,3)
(1,4)
(1,5)
(2,1)
(2,2)
(2,3)
(2,4)
(2,5)
(3,1)
(3,2)
(3,3)
(3,4)
(3,5)
(4,1)
(4,2)
(4,3)
(4,4)
(4,5)
1300
1310
1320
1330
1340

Fig.3

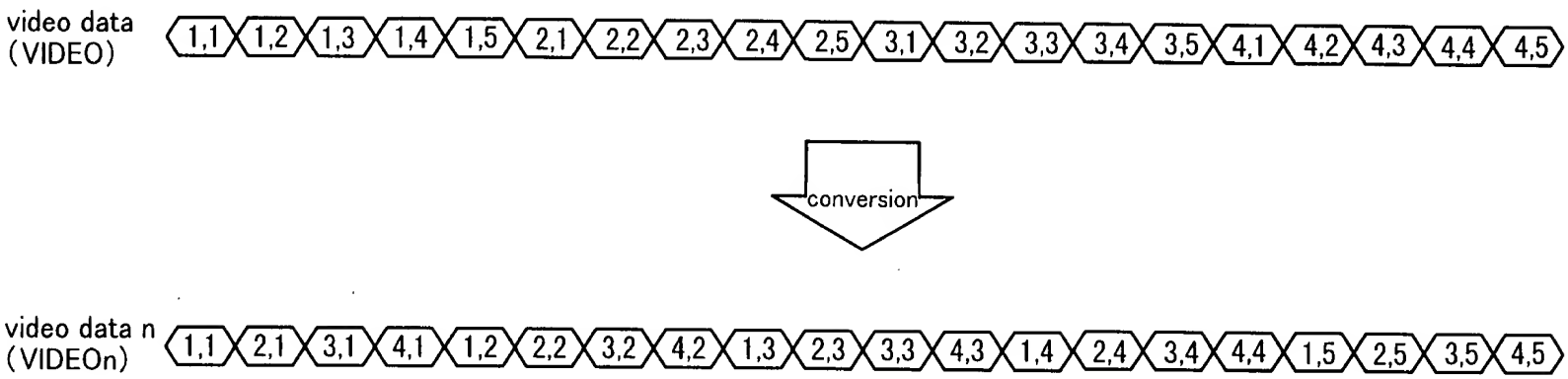

video data
(VIDEO)
1,1 1,2 1,3 1,4 1,5 2,1 2,2 2,3 2,4 2,5 3,1 3,2 3,3 3,4 3,5 4,1 4,2 4,3 4,4 4,5
conversion
video data n
(VIDEOn)
1,1 2,1 3,1 4,1 1,2 2,2 3,2 4,2 1,3 2,3 3,3 4,3 1,4 2,4 3,4 4,4 1,5 2,5 3,5 4,5

Fig.4

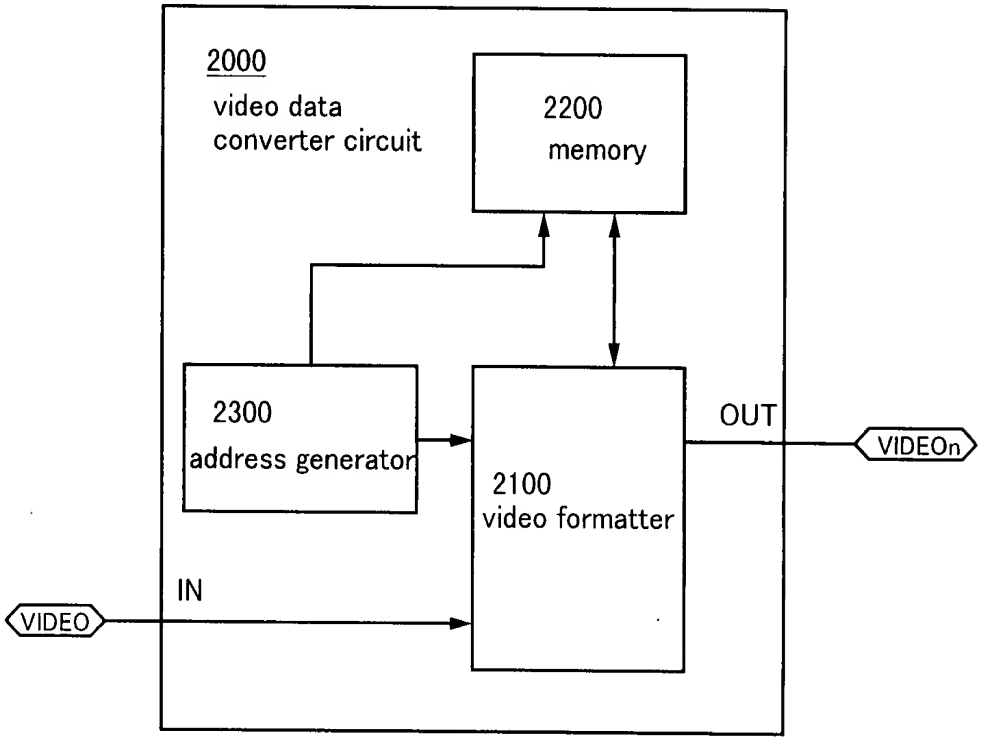

2000
video data
converter circuit
2200
memory
2300
address generator
2100
video formatter
OUT
VIDEOn
IN
VIDEO

Fig.5

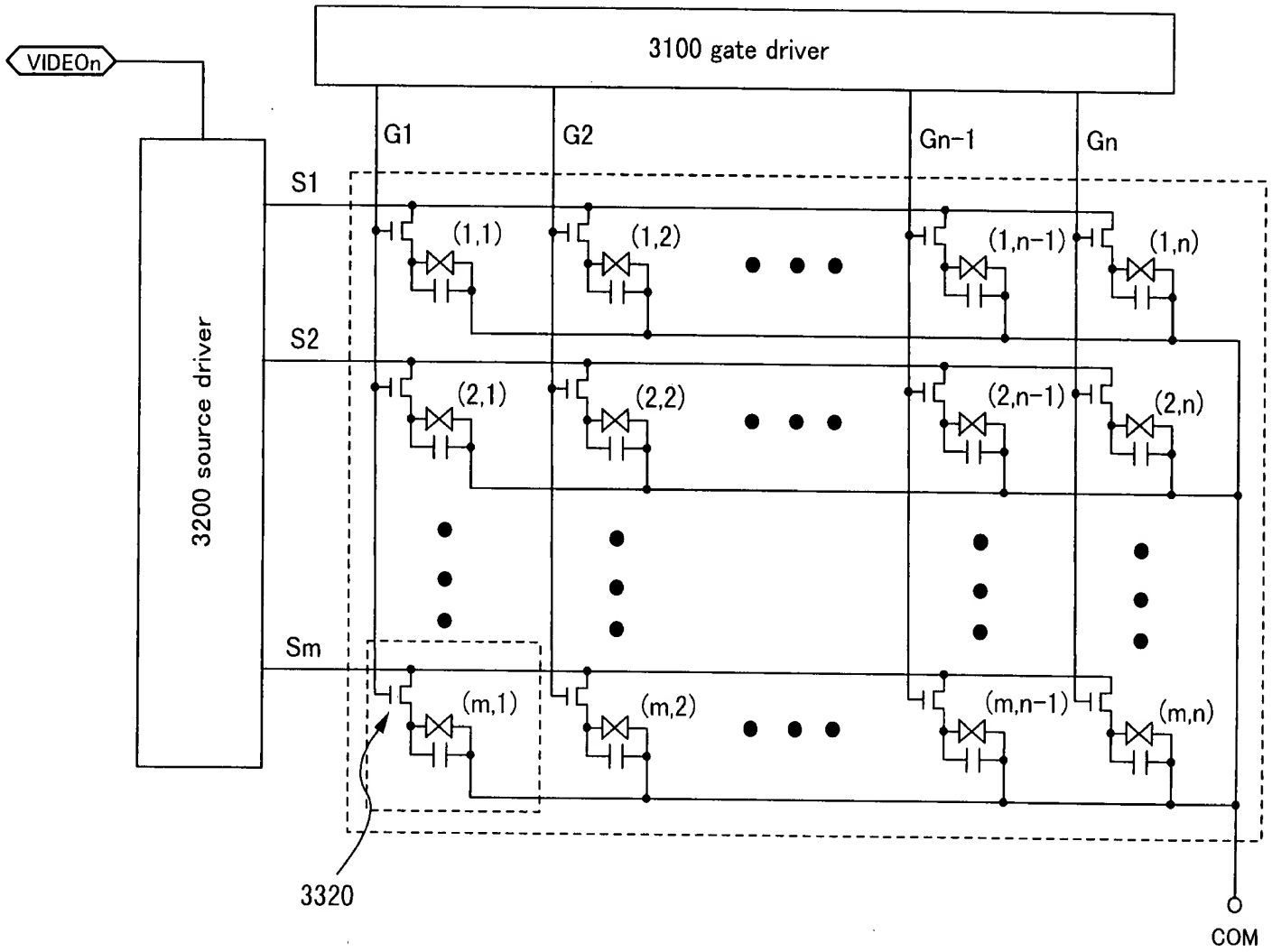

3100 gate driver
VIDEOn
3200 source driver
G1
G2
Gn-1
Gn
S1
S2
Sm
(1,1)
(1,2)
(1,n-1)
(1,n)
(2,1)
(2,2)
(2,n-1)
(2,n)
(m,1)
(m,2)
(m,n-1)
(m,n)
3320
COM

Fig.6

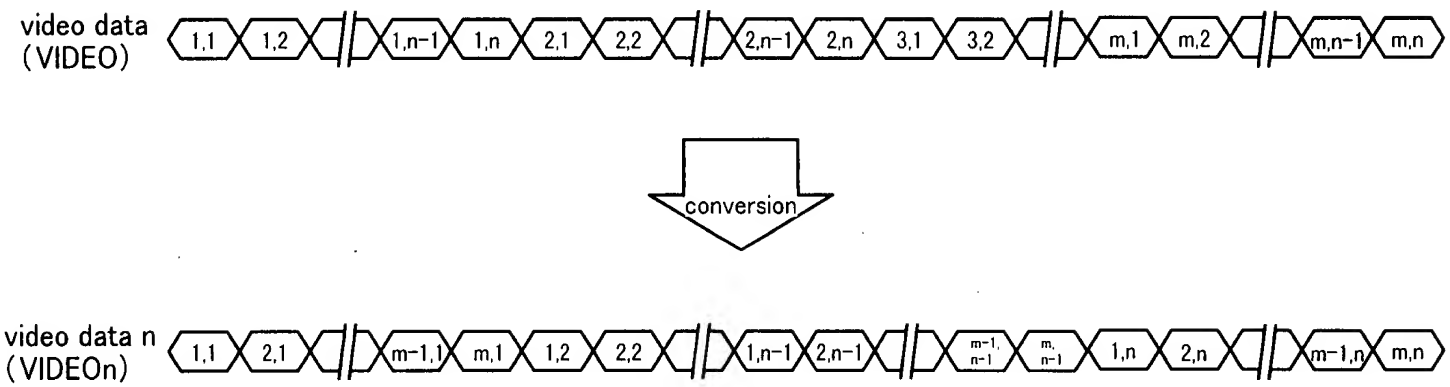

video data
(VIDEO)
1,1
1,2
1,n-1
1,n
2,1
2,2
2,n-1
2,n
3,1
3,2
m,1
m,2
m,n-1
m,n
conversion
video data n
(VIDEOn)
1,1
2,1
m-1,1
m,1
1,2
2,2
1,n-1
2,n-1
m-1,n-1
m,n-1
1,n
2,n
m-1,n
m,n

Fig. 7

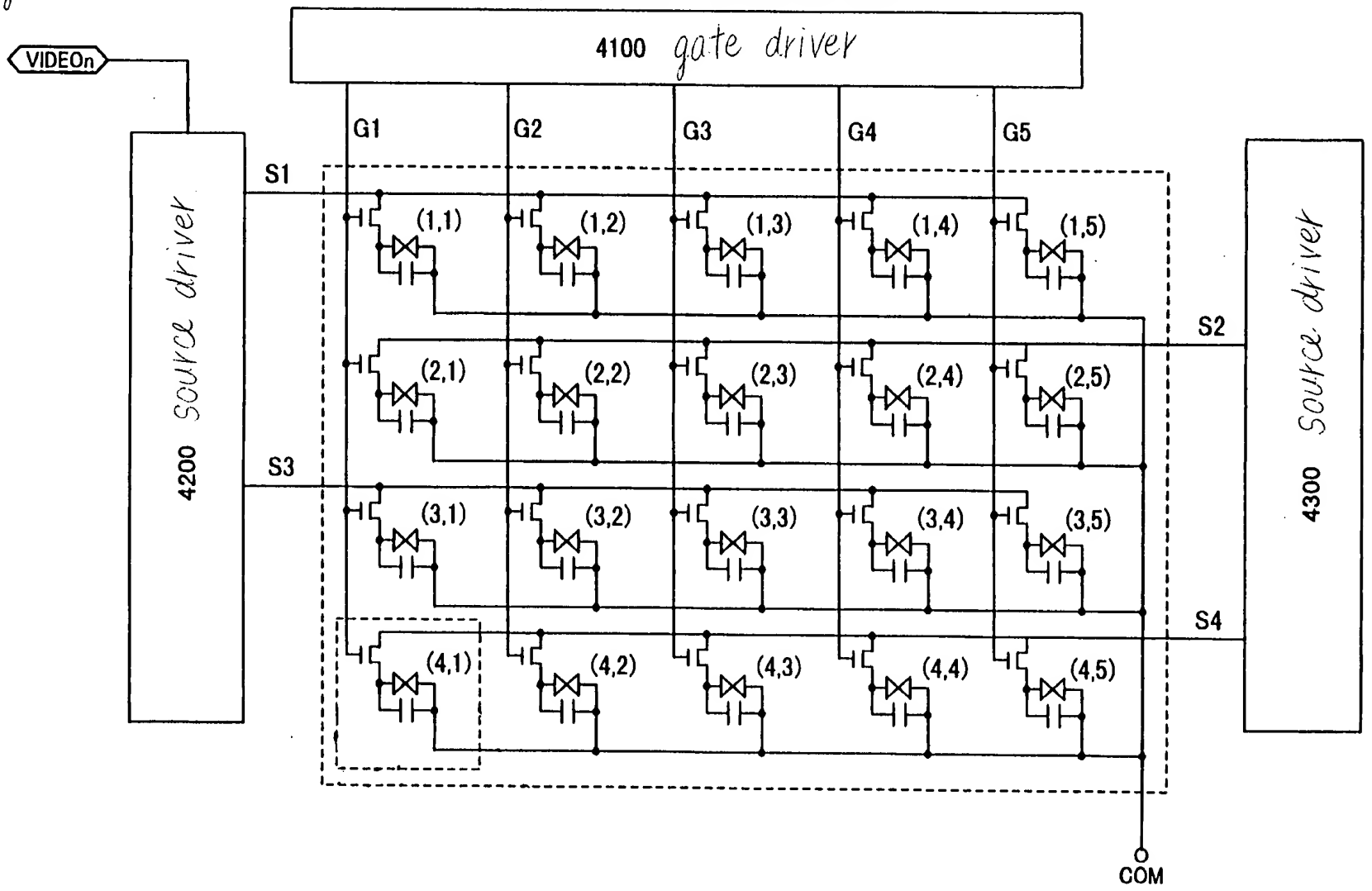

4100 gate driver
VIDEOn
4200 source driver
4300 source driver
G1
G2
G3
G4
G5
S1
S2
S3
S4
(1,1)
(1,2)
(1,3)
(1,4)
(1,5)
(2,1)
(2,2)
(2,3)
(2,4)
(2,5)
(3,1)
(3,2)
(3,3)
(3,4)
(3,5)
(4,1)
(4,2)
(4,3)
(4,4)
(4,5)
COM

Fig.8A

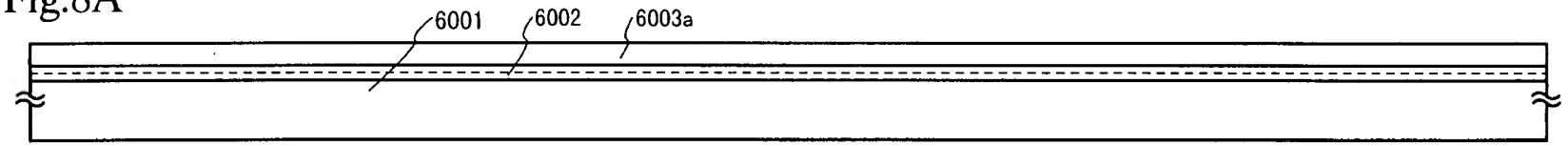


Fig.8B

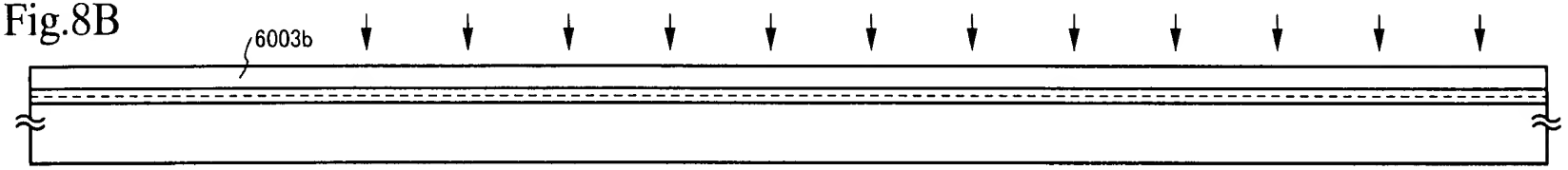


Fig.8C

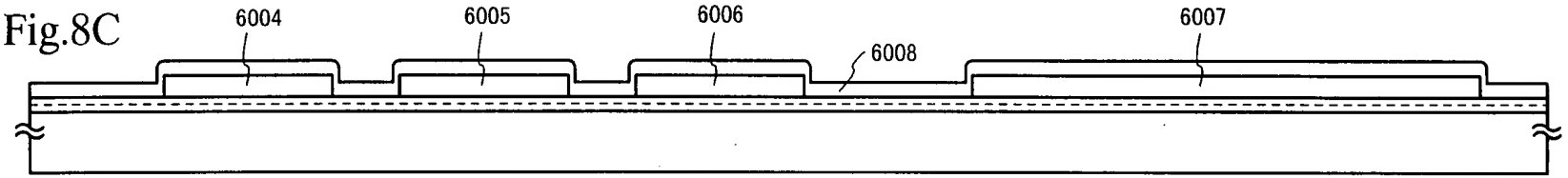


Fig.8D

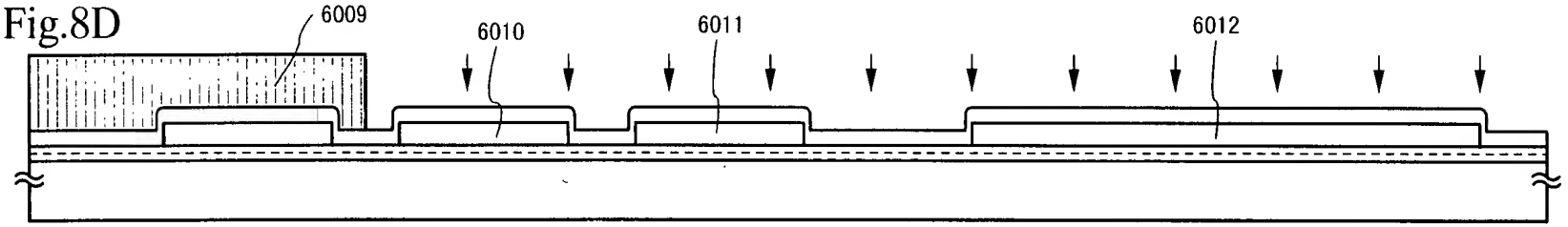


Fig.9A

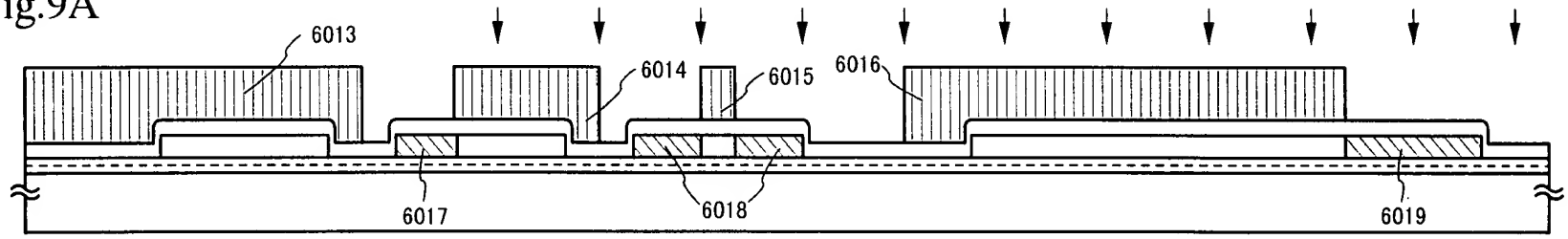


Fig.9B

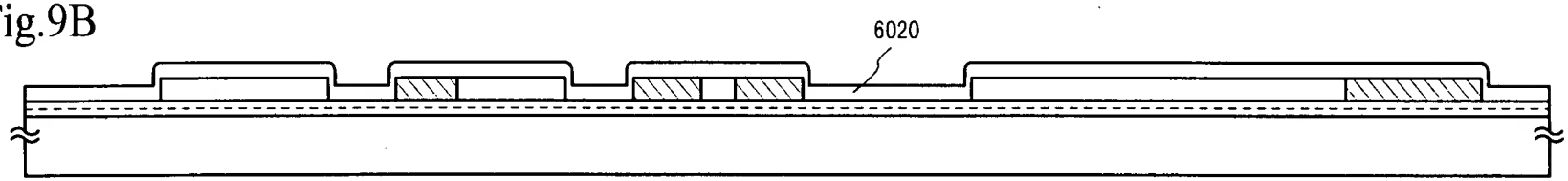


Fig.9C

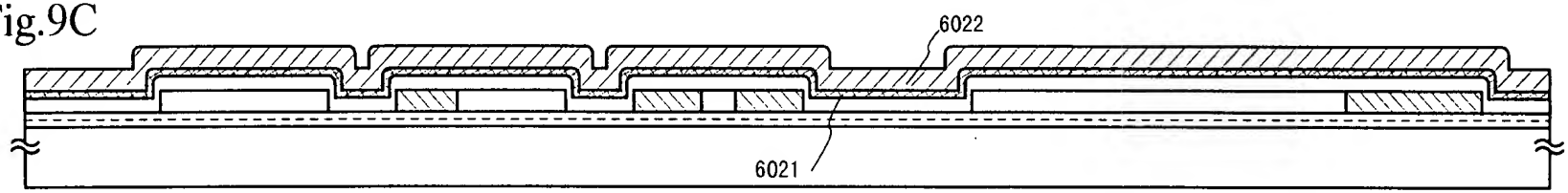


Fig.9D

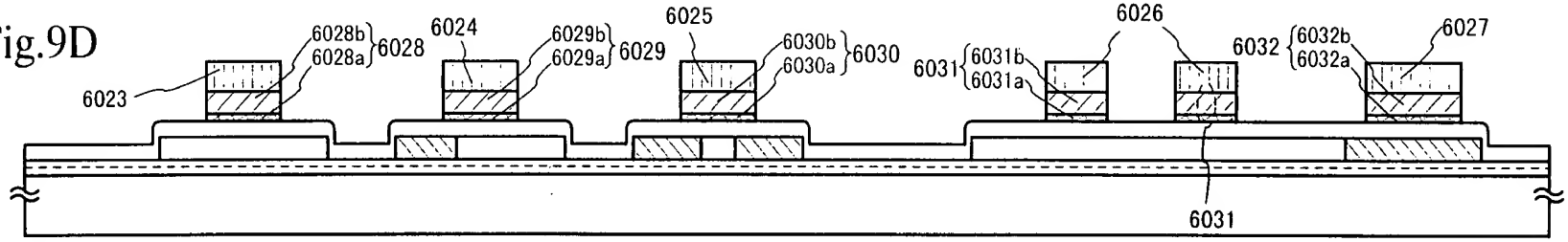


Fig.10A

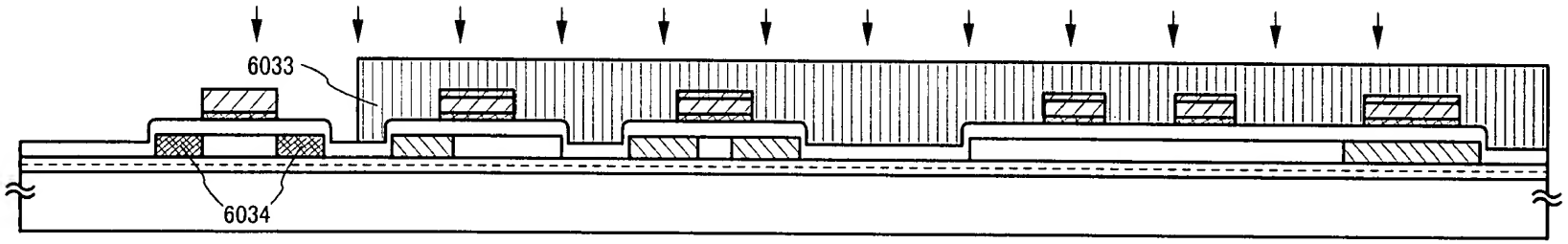


Fig.10B

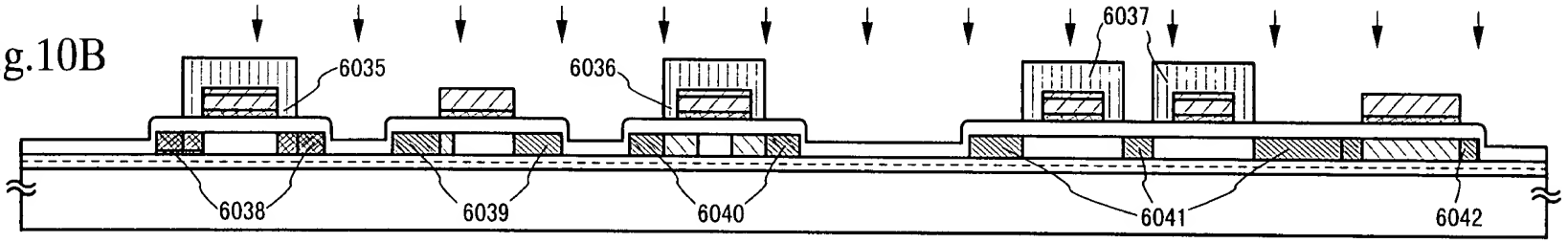


Fig.10C

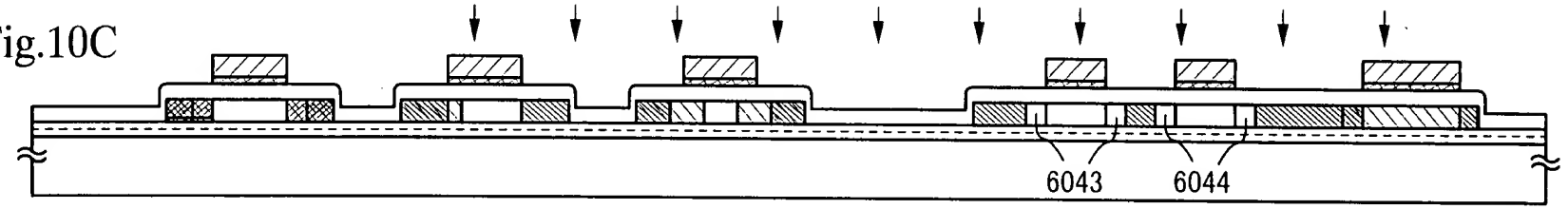


Fig.10D

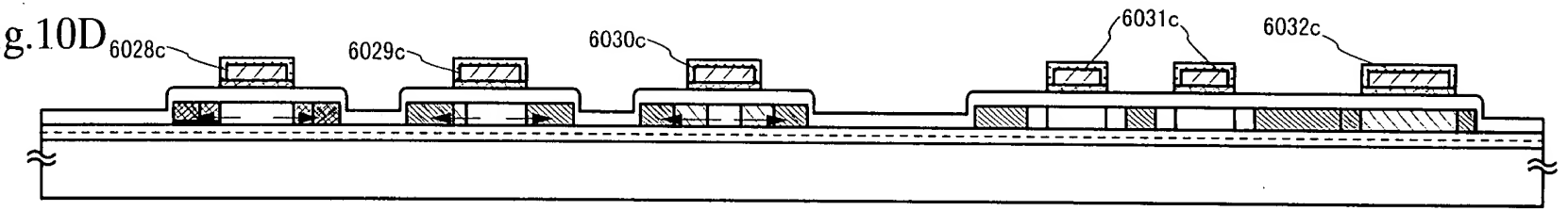


Fig.11A

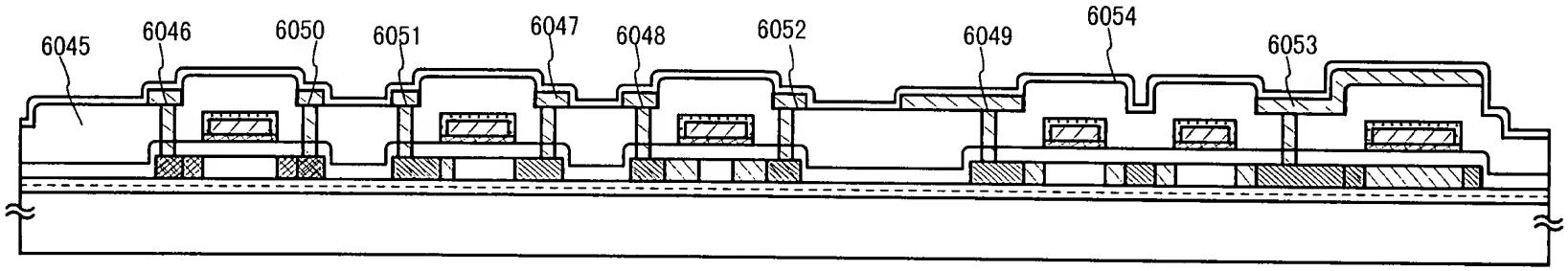


Fig.11B

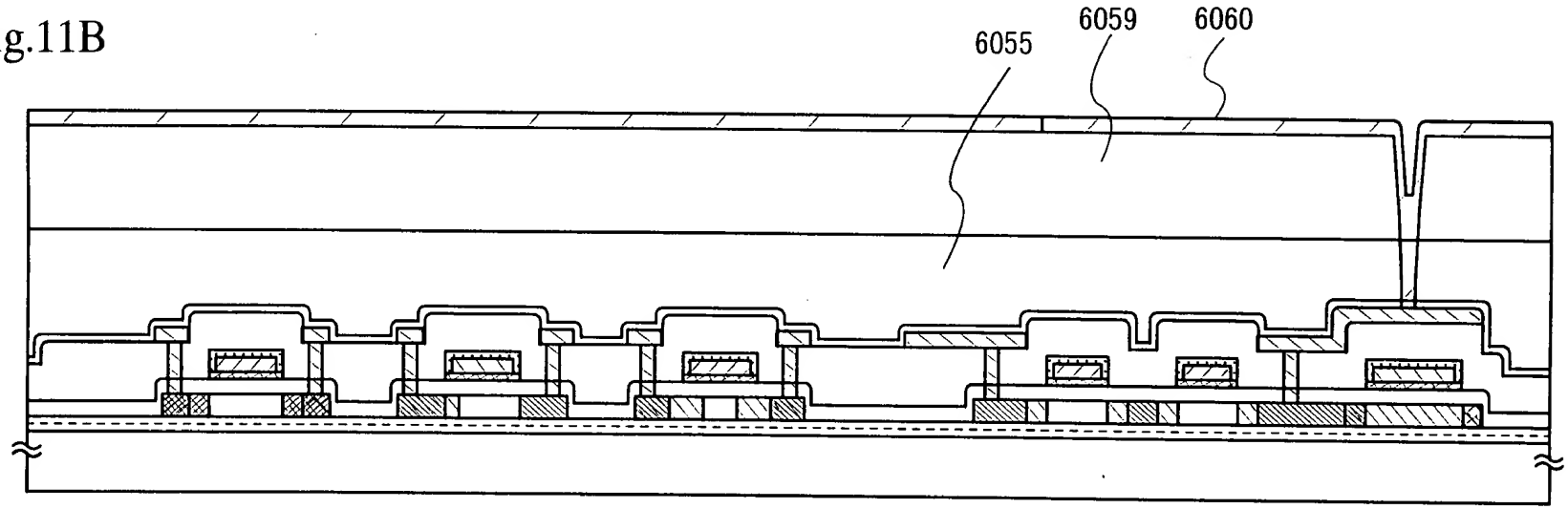
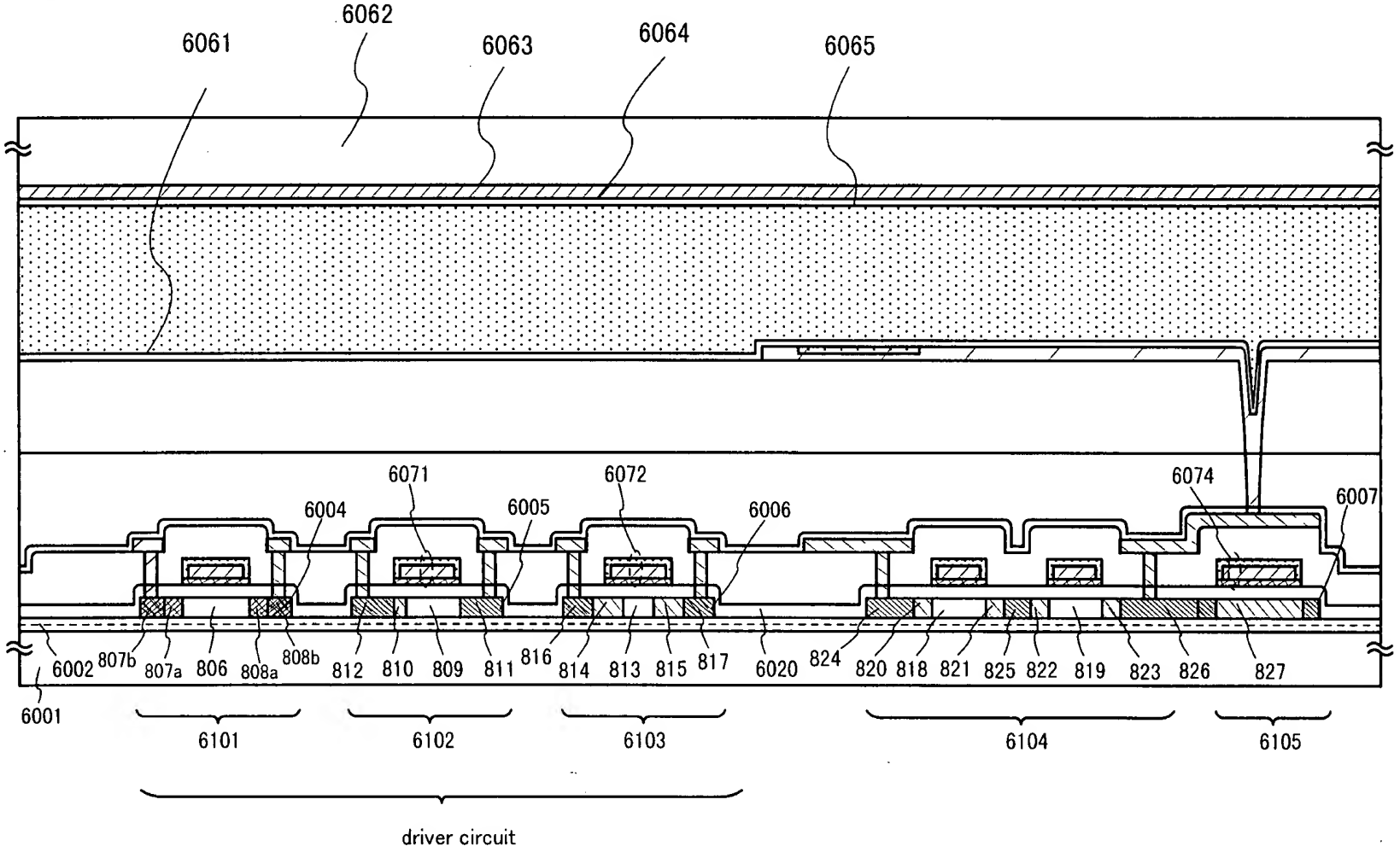


Fig.12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6004
6005
6006
6002
807b
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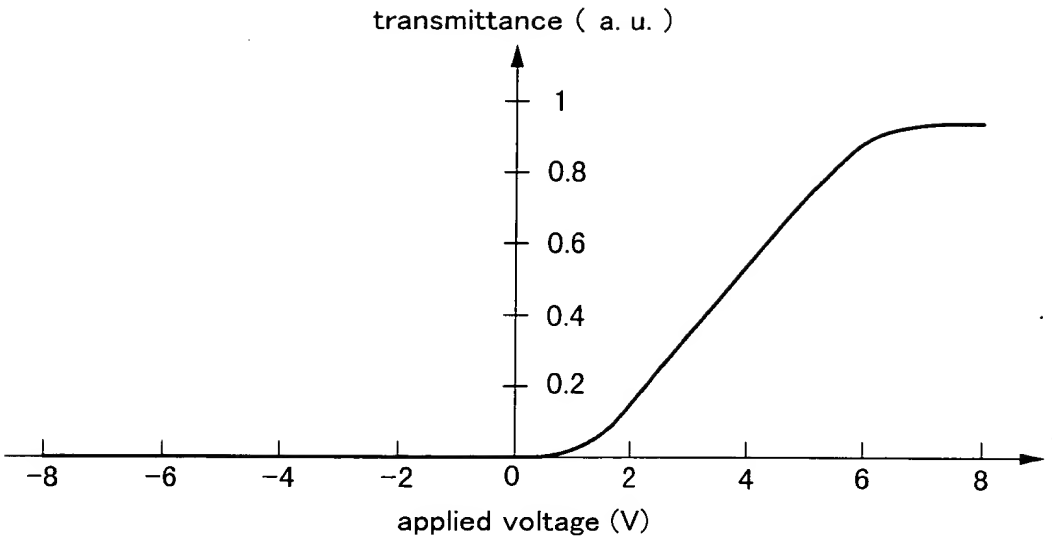
808a
808b
812
810
809
811
816
814
813
815
817
6020
824
820
818
821
825
822
819
823
826
827
6001
6101
6102
6103
6104
6105
driver circuit

Fig.13



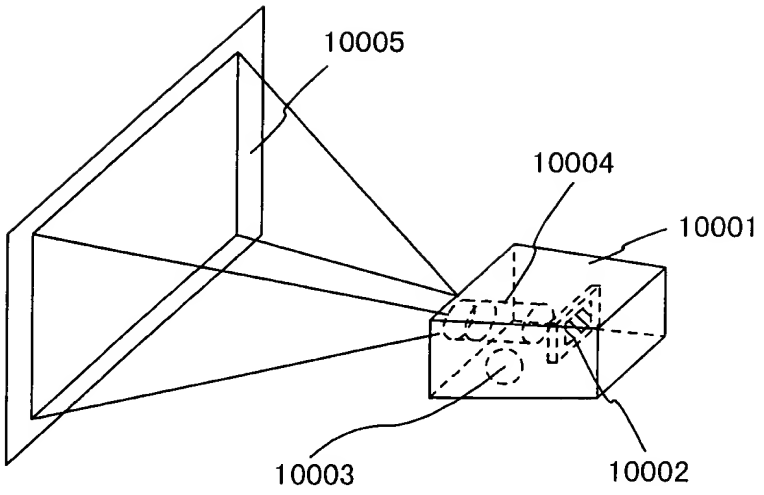


Fig.14A

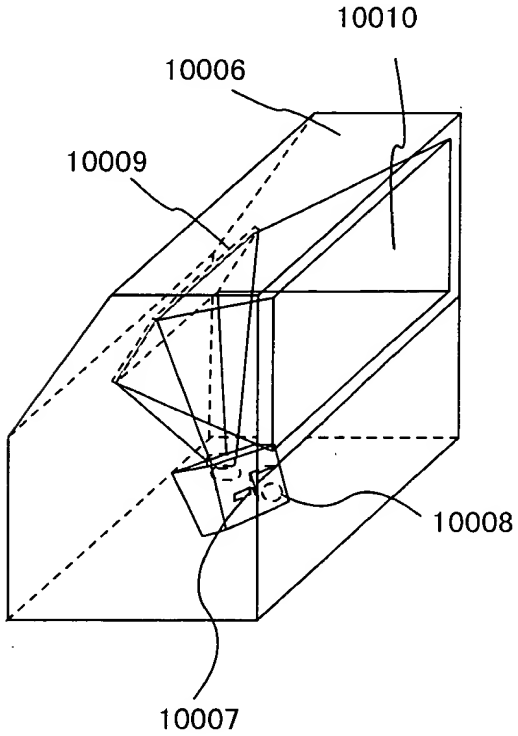


Fig.14B

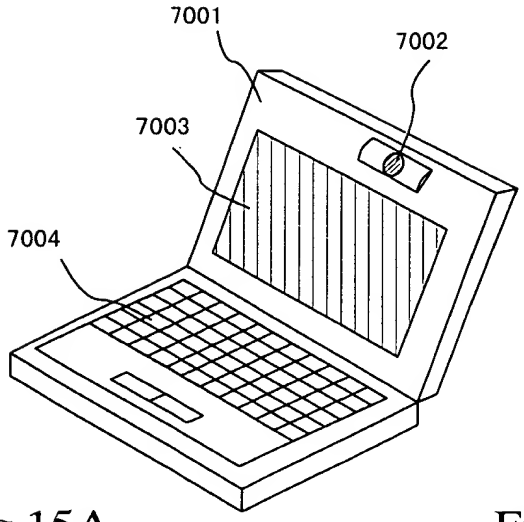


Fig.15A

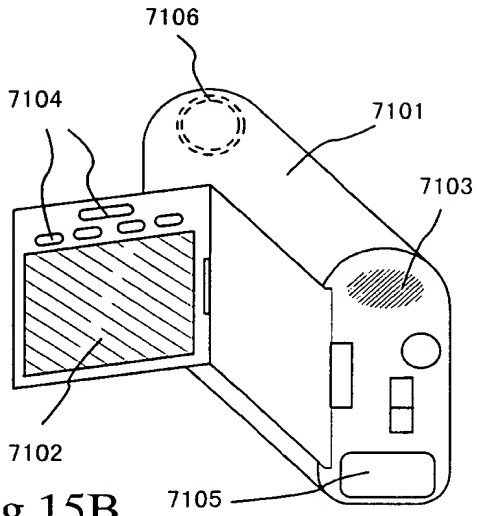


Fig.15B

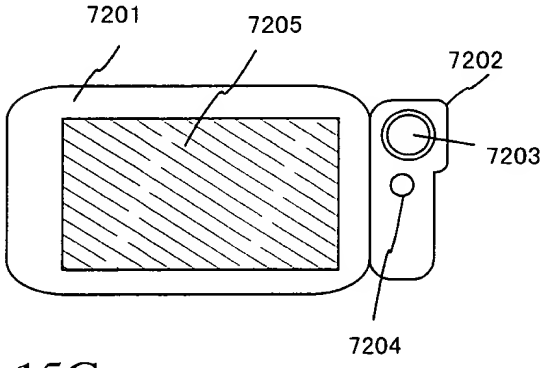


Fig.15C

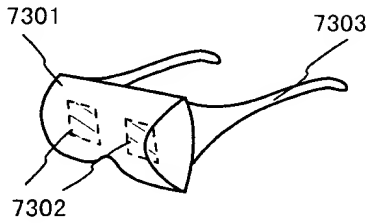


Fig.15D

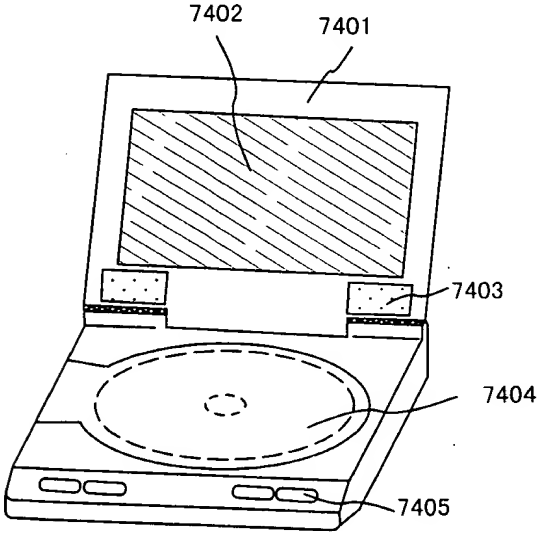


Fig.15E

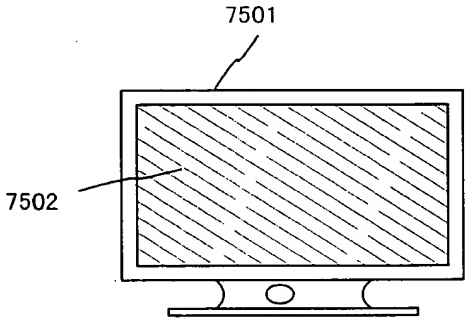
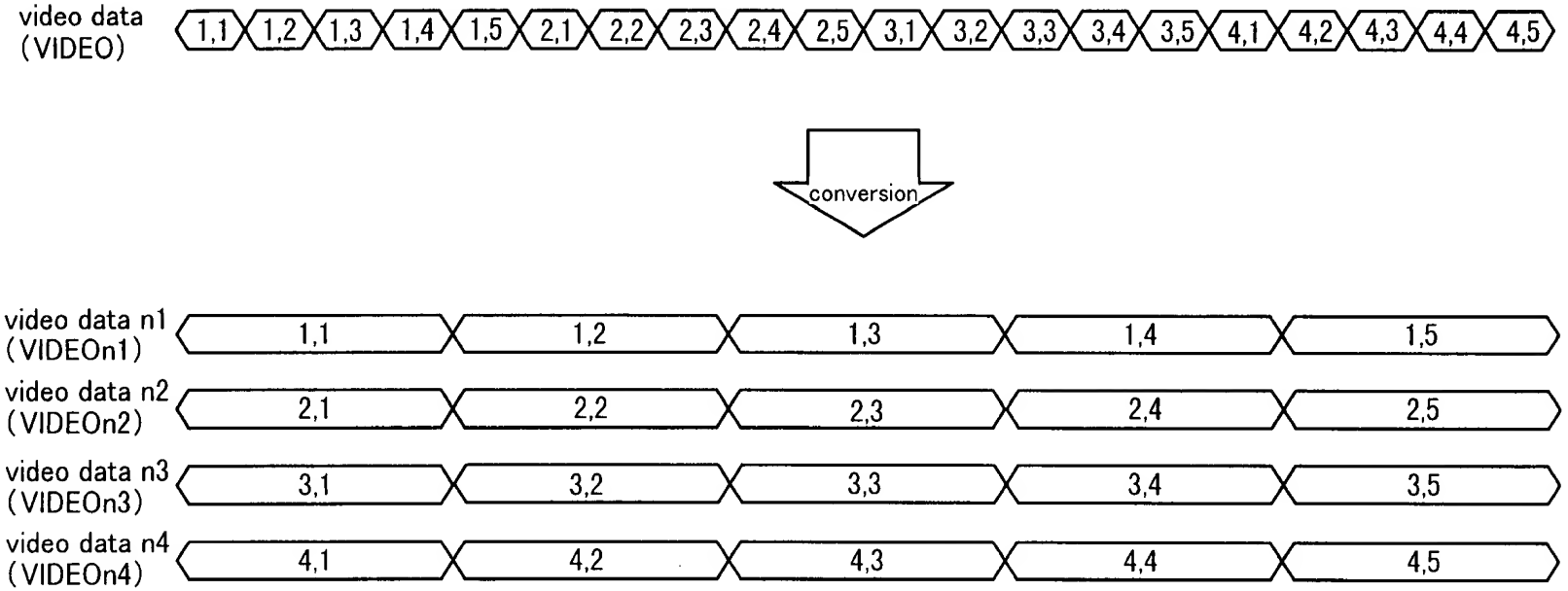


Fig.15F

Fig.16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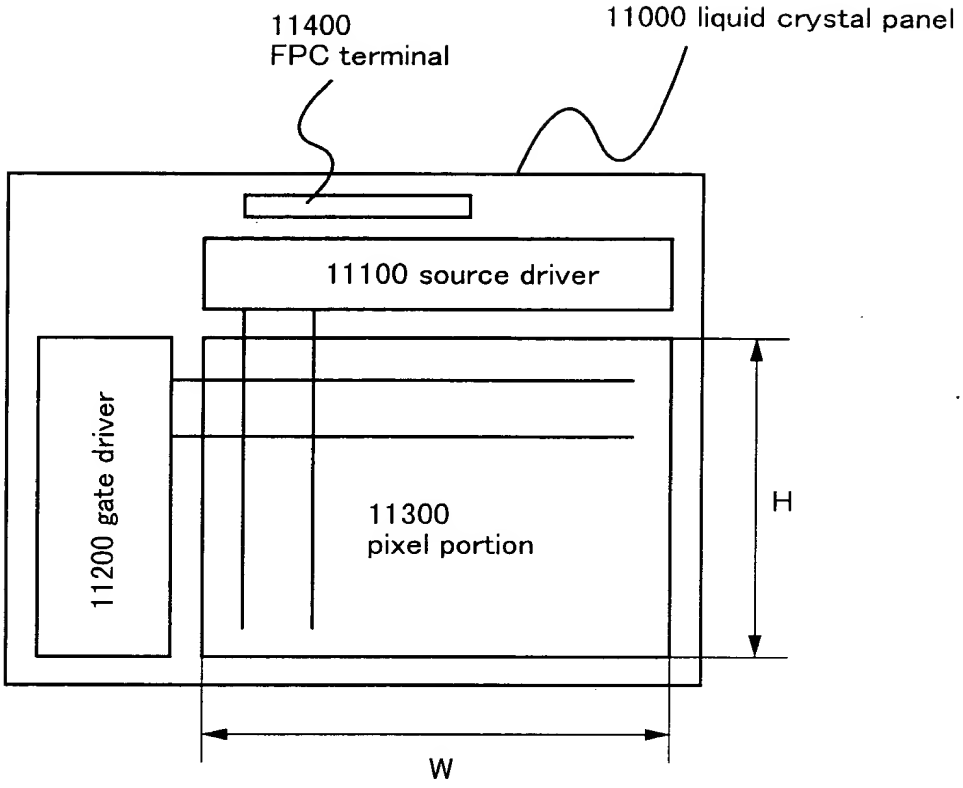
3,1
3,2
3,3
3,4
3,5
4,1
4,2
4,3
4,4
4,5
conversion
video data n1
(VIDEOn1)
1,1
1,2
1,3
1,4
1,5
video data n2
(VIDEOn2)
2,1
2,2
2,3
2,4
2,5
video data n3
(VIDEOn3)
3,1
3,2
3,3
3,4
3,5
video data n4
(VIDEOn4)
4,1
4,2
4,3
4,4
4,5

Fig.17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FPC terminal
11000 liquid crystal panel
11100 source driver
11200 gate driver
11300
pixel portion
H
W